

App. Serial No. 10/564,239
Docket No.: AT030040US1

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In the Claims:

Please amend claims 1-7 and 10 as indicated below. This listing of claims replaces all prior versions.

1. (Currently amended) A circuit for a data carrier for use with a mobile device having an internal power source, the data carrier comprising: ~~which circuit has terminals for connection to at least part of~~

a contact interface via which contact interface a circuit part of the circuit can be the data carrier is supplied with electrical energy from the internal power source; and
processor circuitry ~~which circuit has the circuit part which circuit part is designed to that processes data signals in a normal-consumption processing mode and in an energy-saving processing mode in which less energy is required than in the normal-consumption processing mode, and which circuit part can be the processor circuitry being switched into the energy-saving processing mode when while the energy is being supplied via the contact interface and which circuit part can be being switched, with the aid of in response to a first mode change signal that can be fed thereto, from the energy-saving processing mode into the normal-consumption processing mode; and which circuit has terminals for connection to at least part of~~

a contactless interface via which contactless interface a carrier signal can be is received; by the circuit and

which circuit has a mode change signal generation circuit means which are connected to the contactless interface, and the circuit part and are designed to the mode change signal generation circuit detecting receipt of the carrier signal via the contactless interface and, upon detection of the receipt of the carrier signal to generate generating the first mode change signal in response to detecting the carrier signal, and output the generated first mode change signal to the circuit part.

2. (Currently amended) A data carrier circuit as claimed in claim 1, wherein the mode change signal generation circuit includes means have a carrier signal frequency detection stage to which the received carrier signal can be fed, and are designed, taking account of

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~~the frequency of the received carrier signal to that~~ detects receipt of the carrier signal and ~~to that~~ generates and outputs an indicator signal which indicates that receipt of the carrier signal has been detected.

3. (Currently amended) A data carrier circuit as claimed in claim 1, wherein the mode change signal generation circuit includes means ~~have~~ an interrupt signal generation stage that which are designed, as a result of receipt of the carrier signal being detected, ~~to~~ generates an interrupt signal that represents the first mode change signal.

4. (Currently amended) A data carrier circuit as claimed in claim 1, wherein the ~~circuit~~ part with the aid of data carrier, in response to a second mode change signal that can be fed thereto, is designed to change in a switchable manner from the normal-consumption processing mode to the energy-saving processing mode, and wherein the mode change signal generation circuit means are designed to detect a receive status change from receiving the carrier signal to not receiving the carrier signal and, upon detection of this receive status change, ~~to~~ generates and outputs the second mode change signal.

5. (Currently amended) A data carrier circuit as claimed in claim 4, wherein the mode change signal generation circuit means are designed to outputs the second mode change signal in a manner delayed by a waiting time if, following detection of the receive status change, no new receipt of the carrier signal ~~can be~~ is detected during the waiting time.

6. (Currently amended) A data carrier ~~comprising a circuit~~ as claimed in claim 1, wherein the mobile device is a mobile phone.

7. (Currently amended) A method of changing a processing mode of ~~a circuit for a data carrier, the data carrier for use with a mobile device having an internal power source, and the data carrier including which circuit has terminals for connection to at least part of a contact interface and terminals for connection to at least part of a contactless interface via which contact interface a circuit part of the circuit can be~~ the data carrier is supplied with electrical energy from the internal power source; which circuit part is used, when energy

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~~is being supplied via the contact interface to~~ a contactless interface; and processor circuitry that processes data signals in a normal-consumption processing mode and in an energy-saving processing mode in which less energy is required than in the normal-consumption processing mode, and which circuit part can be the processor circuitry being switched into an the energy-saving processing mode when while the energy is being supplied via the contact interfacc and being which circuit part can be switched, with the aid of in response to a first mode change signal that can be fed thereto, from the energy-saving processing mode into the normal-consumption processing mode, which the method comprising: comprises the following method steps, namely
receiving receipt of a carrier signal via the contactless interface; and
detecting detection of the receipt of the carrier signal; and
generating generation of the first mode change signal upon detection of in response to detecting the receipt of the carrier signal; and
outputting of the generated first mode change signal to the processor circuitry circuit part.

8. (Previously presented) A method as claimed in claim 7, wherein the receipt of the carrier signal is detected taking into account the signal frequency of the carrier signal and the detection of the receipt of the carrier signal is indicated by means of an indicator signal.

9. (Previously presented) A method as claimed in claim 7, wherein upon detection of the receipt of the carrier signal an interrupt signal that represents the first mode change signal is generated.

10. (Currently amended) A method as claimed in claim 7, wherein a receive status change from receiving the carrier signal to not receiving the carrier signal is detected, and wherein upon detection of this receive status change a second mode change signal is generated and outputted to the processor circuitry circuit part and wherein the processor circuitry circuit part upon receiving the second mode change signal is switched from the normal-consumption processing mode to the energy-saving processing mode.

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11. (Previously presented) A method as claimed in claim 10, wherein the second mode change signal is output in a manner delayed by a waiting time if, following detection of the receive status change, no new receipt of the carrier signal is detected during the waiting time.